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STATEMENT BY APPLICANT

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Applicant(s) Markus Szymaniak

Group Art Unit 2672

Examiner Name Ryan R. Yang

U.S. PATENT DOCUMENTS

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OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
Ry		A. SCHILLING, "A processor pipeline for use in graphic data processing," Faculty for Information Technology at the Eberhard-Karls-University at Tubingen, 06-15-1994, 28 pages
Ry		P. BAUDISCH, "Development and implementation of an efficient, hardware-close antialiasing algorithm," Faculty of Information Technology of the Darmstadt College of Technology - Institute for Information Management and Interactive Systems Graphic-Interactive Systems, September 1994, Darmstadt, Germany, 49 pages

EXAMINER SIGNATURE	<i>Ryan Yang</i>	DATE CONSIDERED	08/09/2004
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